

2SC5654

Silicon NPN epitaxial planar type

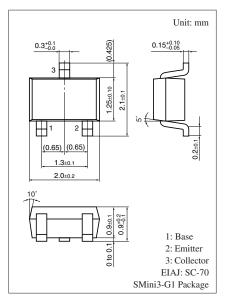
For DC-DC converter

■ Features

- ullet Low collector-emitter saturation voltage $V_{CE(sat)}$
- S-Mini type package, allowing downsizing of the equipment and automatic insertion through the tape packing

■ Absolute Maximum Ratings $T_a = 25$ °C

Parameter	Symbol	Rating	Unit	
Collector-base voltage (Emitter open)	V _{CBO}	20	V	
Collector-emitter voltage (Base open)	V _{CEO}	20	V	
Emitter-base voltage (Collector open)	V_{EBO}	5	V	
Collector current	I_C	1	A	
Peak collector current	I_{CP}	3	A	
Collector power dissipation	P _C	150	mW	
Junction temperature	T_{j}	150	°C	
Storage temperature	T _{stg}	-55 to +150	°C	



Marking Symbol: 2S

■ Electrical Characteristics $T_a = 25$ °C ± 3 °C

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Collector-base voltage (Emitter open)	V _{CBO}	$I_C = 10 \ \mu A, I_E = 0$	20			V
Collector-emitter voltage (Base open)	V _{CEO}	$I_C = 1 \text{ mA}, I_B = 0$	20			V
Emitter-base voltage (Collector open)	V_{EBO}	$I_E = 10 \ \mu A, I_C = 0$	5			V
Forward current transfer ratio	h _{FE}	$V_{CE} = 2 \text{ V}, I_{C} = 100 \text{ mA}$	160		560	_
Collector-emitter saturation voltage	V _{CE(sat)}	$I_C = 200 \text{ mA}, I_B = 10 \text{ mA}$		60	100	mV
Transition frequency	f_T	$V_{CB} = 10 \text{ V}, I_E = -10 \text{ mA}, f = 200 \text{ MHz}$		180		MHz
Collector output capacitance	C _{ob}	$V_{CB} = 10 \text{ V}, I_E = 0, f = 1 \text{ MHz}$		12	30	pF
(Common base, input open circuited)						

Note) Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

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